

Notice of Allowability

Application No.

10/822,524

Examiner

Russell M. Kobert

Applicant(s)

HUANG ET AL.

Art Unit

2829

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to April 12, 2004.
2. ☒ The allowed claim(s) is/are 1-8.
3. ☒ The drawings filed on 12 April 2004 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 0404
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

Reasons For Allowance

1. The following is an examiner's statement of reasons for allowance:

An apparatus and method for reducing noise and power supply instability problems during Optical Beam Induced Resistance Change (OBIRCH) analysis testing of circuit elements on an integrated circuit comprising the combination of:

- An integrated circuit to be tested connected to an OBIRCH analysis circuit
- A diode circuit connected across positive and negative outputs of a power source wherein the diode circuit comprises a common or bi-polar diode having its anode connected to the positive output of the power source and its cathode connected to the anode of a plurality of Schottky diodes serially connected, cathode-to-anode, the negative output of the power source being coupled to the cathode of the end Schottky diode of the plurality of serially connected diodes such that a precise and different voltage selection is available at the anode of each Schottky diode of the series wherein the integrated circuit to be tested is connected to one of the precise voltage selections as its core or operating voltage;

As further detailed in claims 1 and 5 has not been found.

It is further noted that the examiner's reasons are understood to be predicated upon consideration of each of the claims as a whole, and not upon any specific elements of the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

2. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Reay (5015889) shows Schottky diodes serially connected wherein a voltage between two of the diodes is connected to a DUT.

Kamiya (6218882) shows a plurality of Schottky diodes serially connected to produce discrete voltages at capacitors having one end connected to each connecting point of the plurality of diodes.


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Russell Kobert whose telephone number is (571) 272-1963.

The Examiner's Supervisor, Nestor R. Ramirez, can be reached at (571) 272-2034.

For an automated menu of Tech Center 2800 phone numbers call (571) 272-2800.



Russell M. Kobert
Patent Examiner
Group Art Unit 2829
May 31, 2005



VINH NGUYEN
PRIMARY EXAMINER
05/31/05